

IN THE CLAIMS

Please add new claims 8-11, as follows:

1 --8. A display device with a power interruption delay function, comprising:

2 a pulse width modulation controller for generating a pulse width modulation signal under the
3 control of a microcomputer;

4 a horizontal deflection coil for horizontally deflecting electron beams generated in said
5 display device;

6 a current amplification transformer having a primary coil and a secondary coil;

7 a field effect transistor having its gate terminal connected to one terminal of said secondary
8 coil;

9 one terminal of said primary coil being connected to an output terminal of said pulse width
10 modulation controller through a capacitor and another terminal of said primary coil being connected
11 to a ground terminal;

12 said field effect transistor having a drain terminal connected to a high voltage source and a
13 source terminal connected in common to a second terminal of said secondary coil and one side of
14 a pulse transformer;

15 said pulse transformer having a second side connected to one side of said horizontal
16 deflection coil;

17 a first diode connected between said source terminal and said drain terminal; and

18 a second diode connected between said second terminal of said secondary coil and said

19 ground terminal;

20 a H/V processor for generating a square wave pulse signal under the control of said
21 microcomputer;

22 a horizontal driver for generating a drive pulse signal in response to the square wave pulse
23 signal from said H/V processor;

24 an S-correction capacitor connected in series between said horizontal deflection coil and a
25 ground terminal, for correcting a linearity of center-to-left and right sides of a screen;

26 a horizontal output circuit for charging and discharging energy on said horizontal deflection
27 coil and said S-correction capacitor in response to an output signal from said current amplifier and
28 said drive pulse signal from said horizontal driver;

29 a H/V processor constant voltage circuit for supplying a constant voltage to said H/V
30 processor in response to an input voltage; and

31 power interruption delay charging means for gradually lowering said input voltage to said
32 H/V processor constant voltage circuit when power supplied to said display device is interrupted.

1 9. The display device as set forth in claim 8, wherein said power interruption delay
2 charging means includes:

3 a polarity capacitor for performing a charging operation when power is supplied to said
4 display device and a discharging operation when the power supplied to said display device is
5 interrupted; and

6 a diode connected to said polarity capacitor, for preventing a voltage charged on said polarity

capacitor from being discharged to a power supply circuit when the power supplied to the display device is interrupted.

10. The display device as set forth in claim 8, wherein said horizontal output circuit comprises a horizontal output transistor having a collector terminal connected in common to said second side of said pulse transformer and said one side of said horizontal deflection coil, an emitter terminal connected to said S-correction capacitor and said ground terminal, and a base terminal connected to an output terminal of said horizontal driver for receiving said drive pulse signal.

11. The display device as set forth in claim 10, wherein said horizontal driver comprises:
a second field effect transistor having a gate terminal connected to receive said square wave pulse signal from said H/V processor, a source terminal connected to said ground terminal, and a drain terminal;

a horizontal drive transformer having a primary coil and a secondary coil, said primary coil having one terminal connected to a voltage source through a resistor and a second terminal connected to said drain terminal of said second field effect transistor; and

said secondary coil of said horizontal drive transformer having one side connected to said base terminal of said horizontal output transistor and a second side connected to said ground terminal.--